

REMARKS

The Applicant's representative has carefully reviewed and considered the Office Action mailed on September 10, 2003, as well as the references cited therewith. No claims have been amended, claims 4-5 are canceled, and no claims are added. As a result, claims 2-3, 6-7, and 9-21 are now pending in this application.

§103 Rejection of the Claims

Claims 4-5 were rejected under 35 USC § 103(a) as being unpatentable over IBM Technical Disclosure Bulletin, Vol. 20, no. 8, pp. 3117-3118, 1978 (hereinafter "3117"). However, since claims 4-5 have been canceled in this Response without disclaimer or prejudice, the rejection is moot.

Allowable Subject Matter

The Applicant notes with appreciation that claims 2-3, 6-7, and 9-21 have been allowed.

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Serial Number: 09/537274

Filing Date: March 29, 2000

Title: MULTI-LAYER CHIP CAPACITOR

Assignee: Intel Corporation

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Dkt: 884.240US1 (INTEL)

CONCLUSION

The Applicant respectfully submits that all of the pending claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone the Applicant's attorney, Mark Muller (210-308-5677), or the undersigned at 612-373-6970 to facilitate prosecution of this application. If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

LARRY E. MOSLEY

By his Representatives,

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Date Oct. 23, 2003

By Ann M. McCrackin

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS AF, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 23 day of October 2003.

Name

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Signature

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